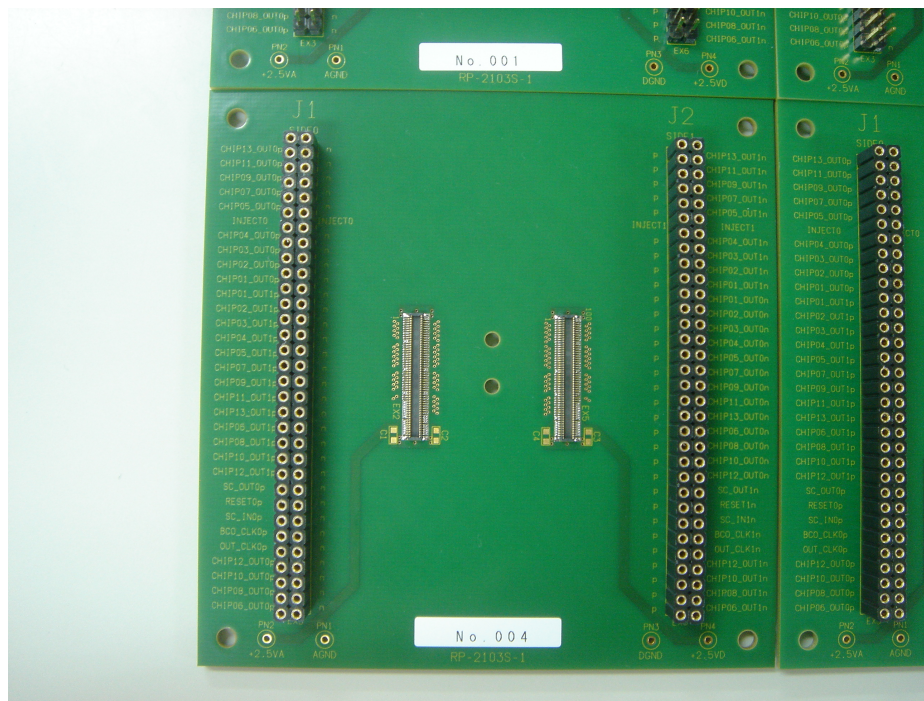
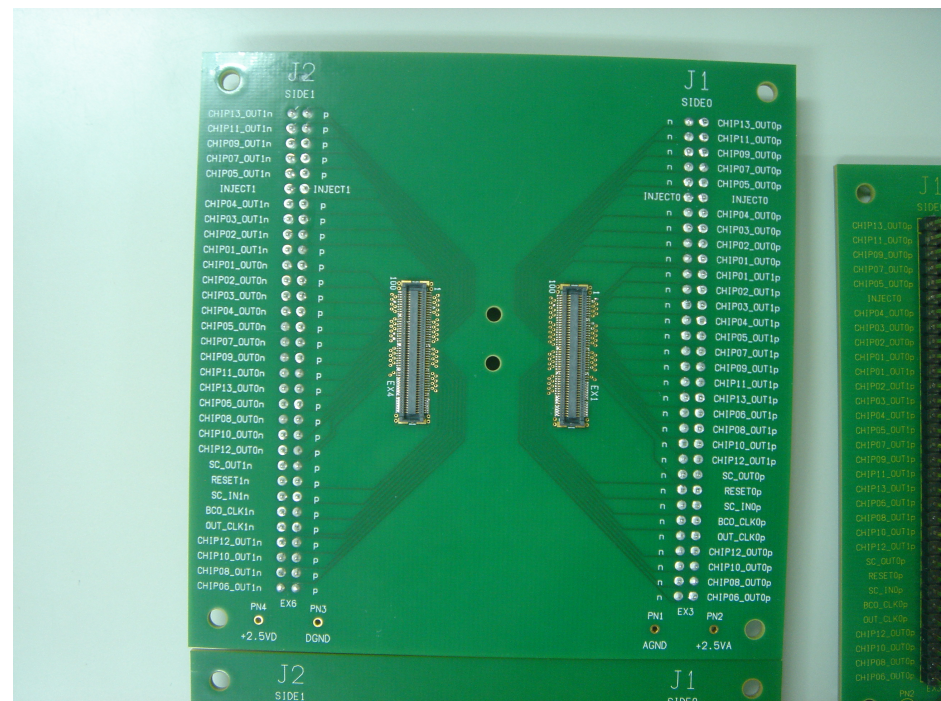


Interception Boards

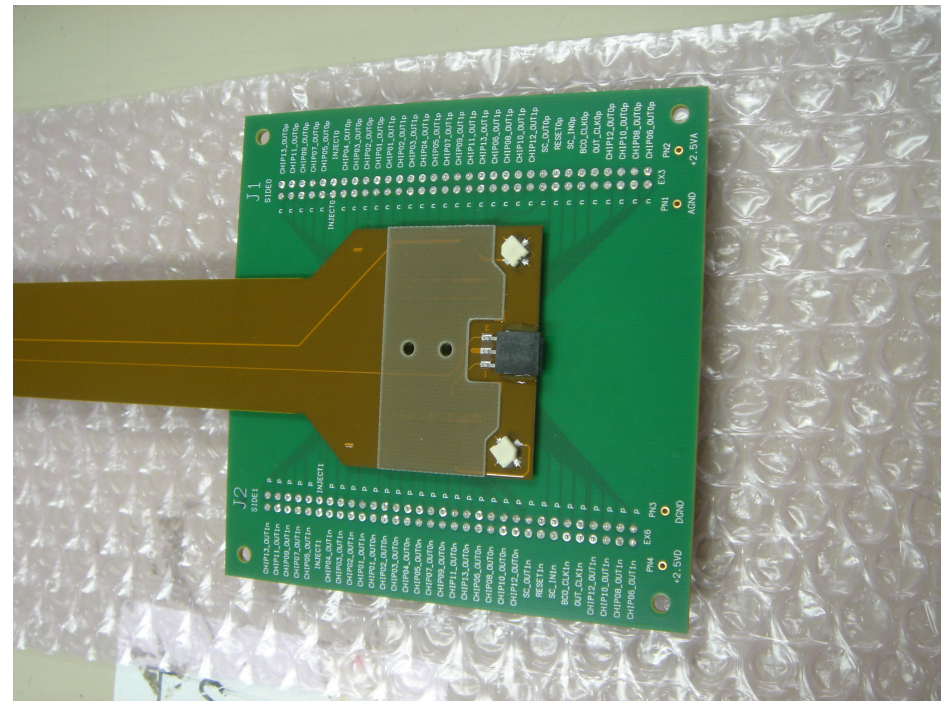
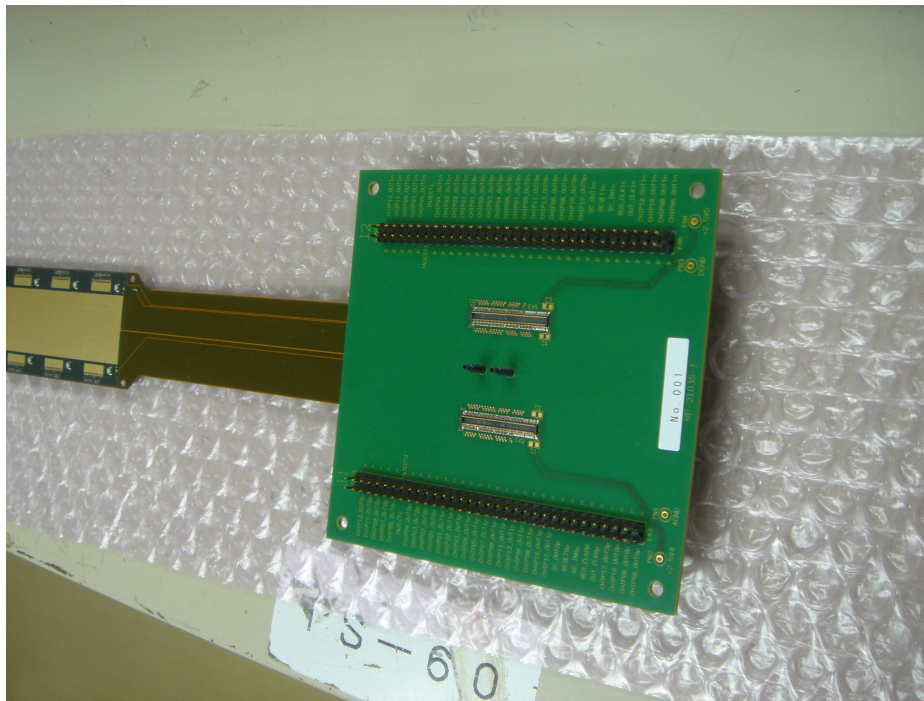


Front Side



Back Side

Interception Board with HDI cable



Half Entry Issue Debugging

- Insert the interception board between the bus extender and conversion cable and take calibration data.
 - Make sure the effect of the interception board to the calibration results. May need the LVDS amplitude scan to evaluate the effect.
 - Check the LVDS signal of good FPHX chips to keep “a good example”.
- Monitor LVDS signals with the bus extender.
 - See if there is dead LVDS lines in Chip-12, 24, and 26.
 - Reproduce empty and half entry at 2mA/4mA/7mA/8mA and monitor the signal.
 - Correlate with half entry issue with the observed signal.
- Play with Single/Double LVDS pair mode while monitoring LVDS signal with a scope of these LVDS lines.

What to monitor

- Check the baseline of the LVDS signals.
- Play with initialization sequence if there is any change to the LVDS outputs. Power on/off may also be worthy to try.
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