

# Felix を使ったバレルラダーテスト 糠塚元気 (RBRC)

Super simple DAQ monitor

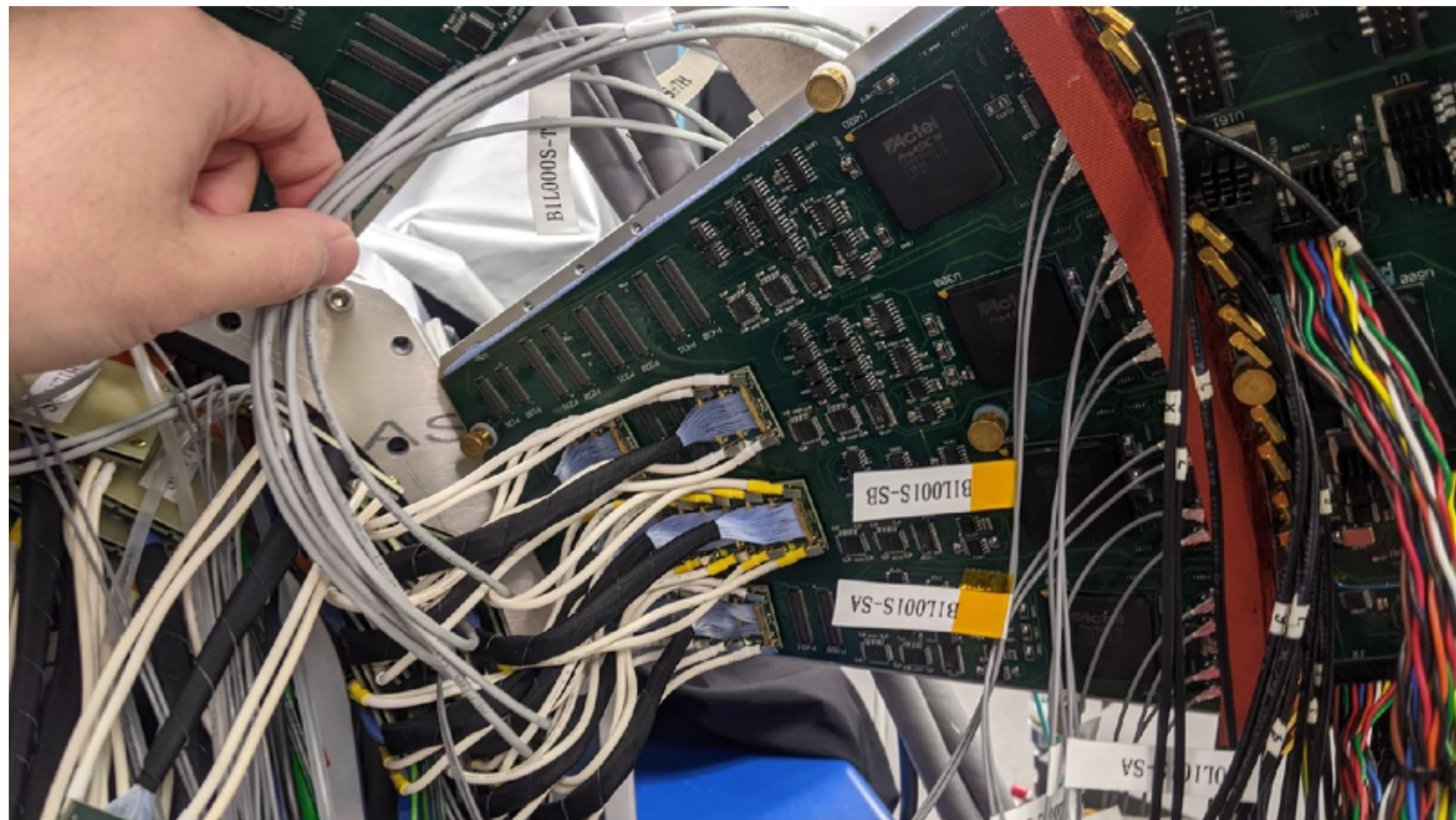
HV control GUI  
(to be replaced by our application)

The new DAQ for the Felix system  
at the silicon lab

The screenshot shows two overlapping windows. The left window, 'Easy LV | HV', displays a graph with voltage on the y-axis (0.0 to 80.0) and time on the x-axis (12:22:00.000 to 12:25). Below the graph is a table with columns for Channel, Status, On/Off, Vset [V], Iset [nA], and Vmeas [V].

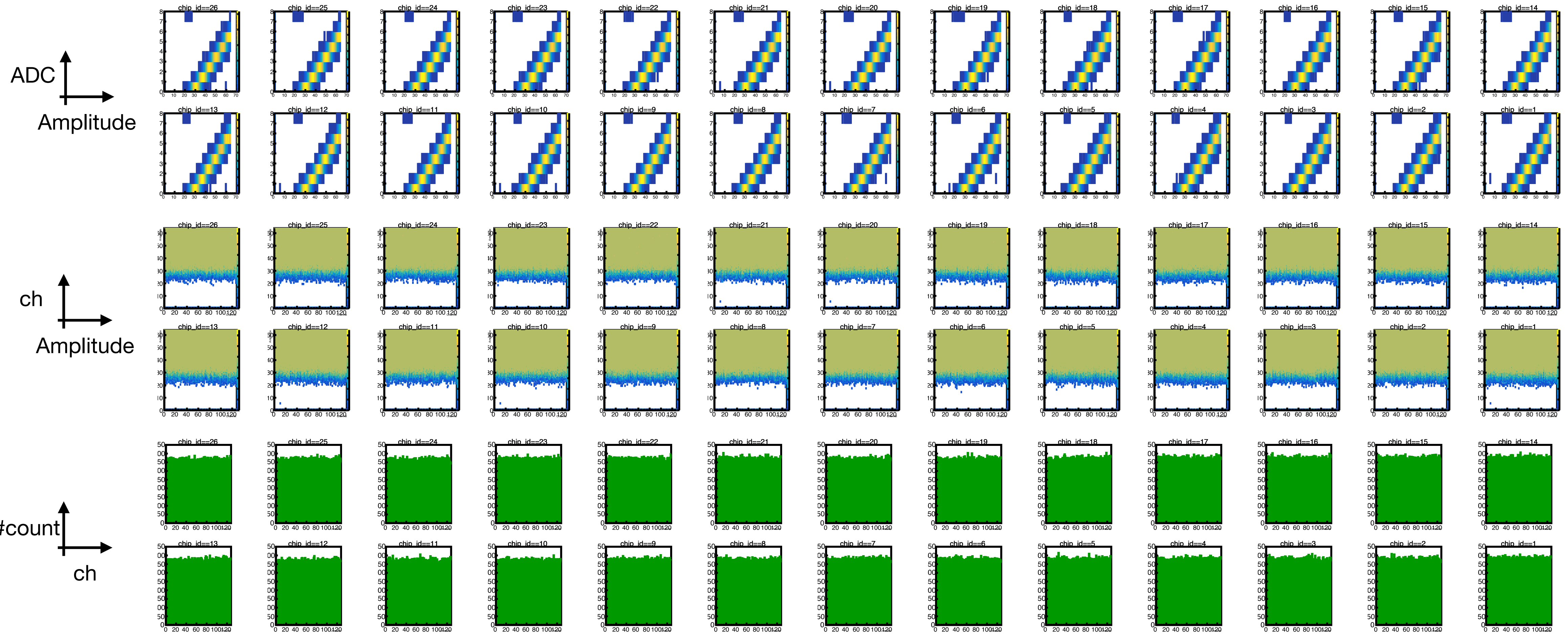
Channel	Status	On/Off	Vset [V]	Iset [nA]	Vmeas [V]
WIENER MPOD - 1287029	-	-	-	-	-
E08F0 - 741570 - slot 0	✓	<crate>	<crate>	<crate>	-
E08F0 - 741860 - slot 1	✓	<module>	<module>	<module>	-
Channel 0	✓	Constant Volt.	100	1e+06	100.011
Channel 1	✓	Constant Volt.	100	1e+06	99.9901
Channel 2	✓	Constant Volt.	100	1e+06	100.001
Channel 3	✓	Constant Volt.	100	1e+06	100.001
Channel 4	✓	Constant Volt.	100	1e+06	100.001

The right window, 'Simple GUI for the Felix server', features a dark theme with numerous yellow buttons for operations like 'load\_data\_size', 'start\_sm', 'reset\_fpga', 'reset\_fpx', 'latch\_fpga', 'calib', 'enable\_io', 'enable\_channel', 'disable\_channel', 'macro\_calib', 'send\_calib\_param', 'send\_fpx\_cmd', 'make\_fpx\_cmd', 'ld\_fpxparam', 'write\_soparam', 'read\_fpx', 'write\_fpx', 'cold\_start', 'check\_register', and 'dam\_reset'. It also includes a 'Raw commands' section and a 'File' browser.



All operations (DAQ, HV, LV) were driven in the new Felix server at the silicon lab.

# 7-ladder Calibration, B1 L000S



# 7-ladder Calibration

