DAQ Trouble of Stream readout Issue

RIKEN/RBRC

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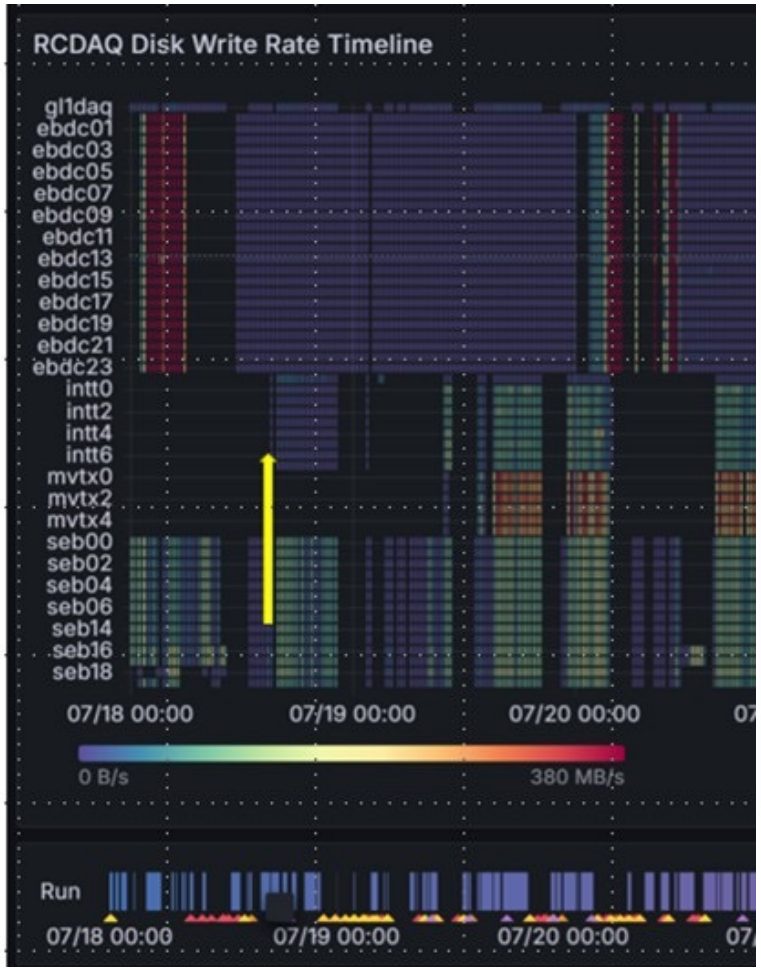
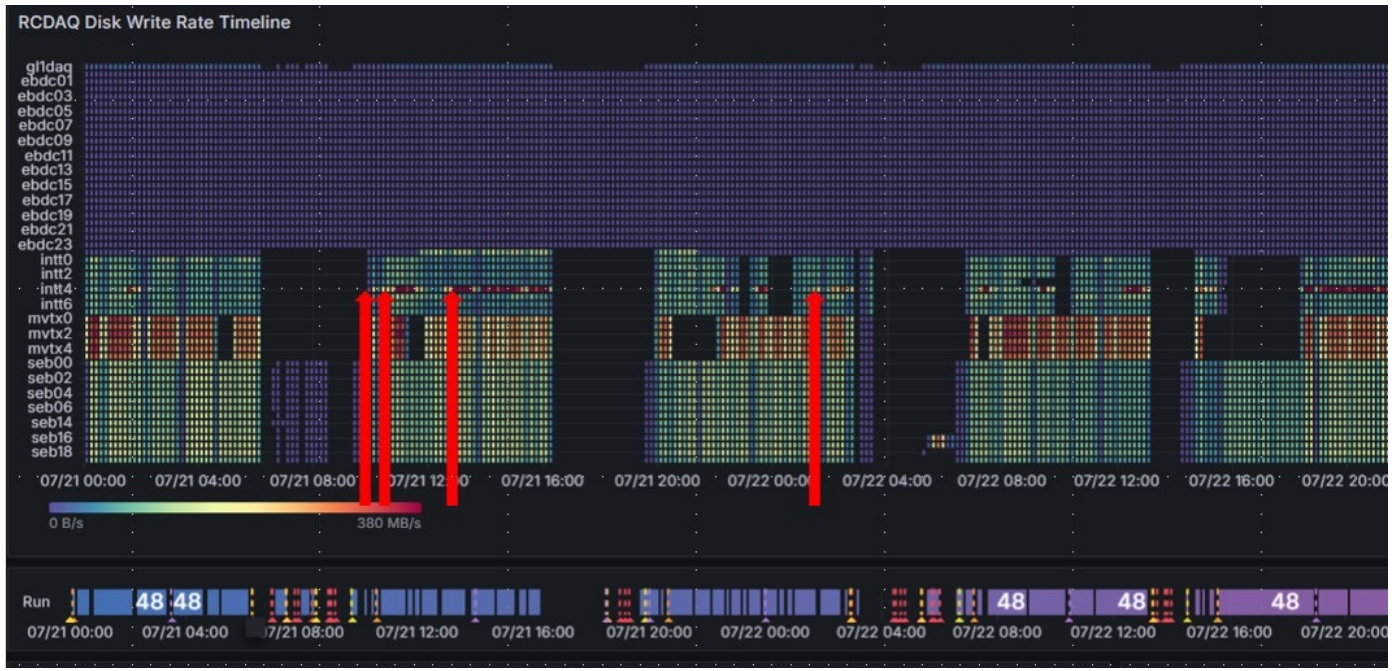
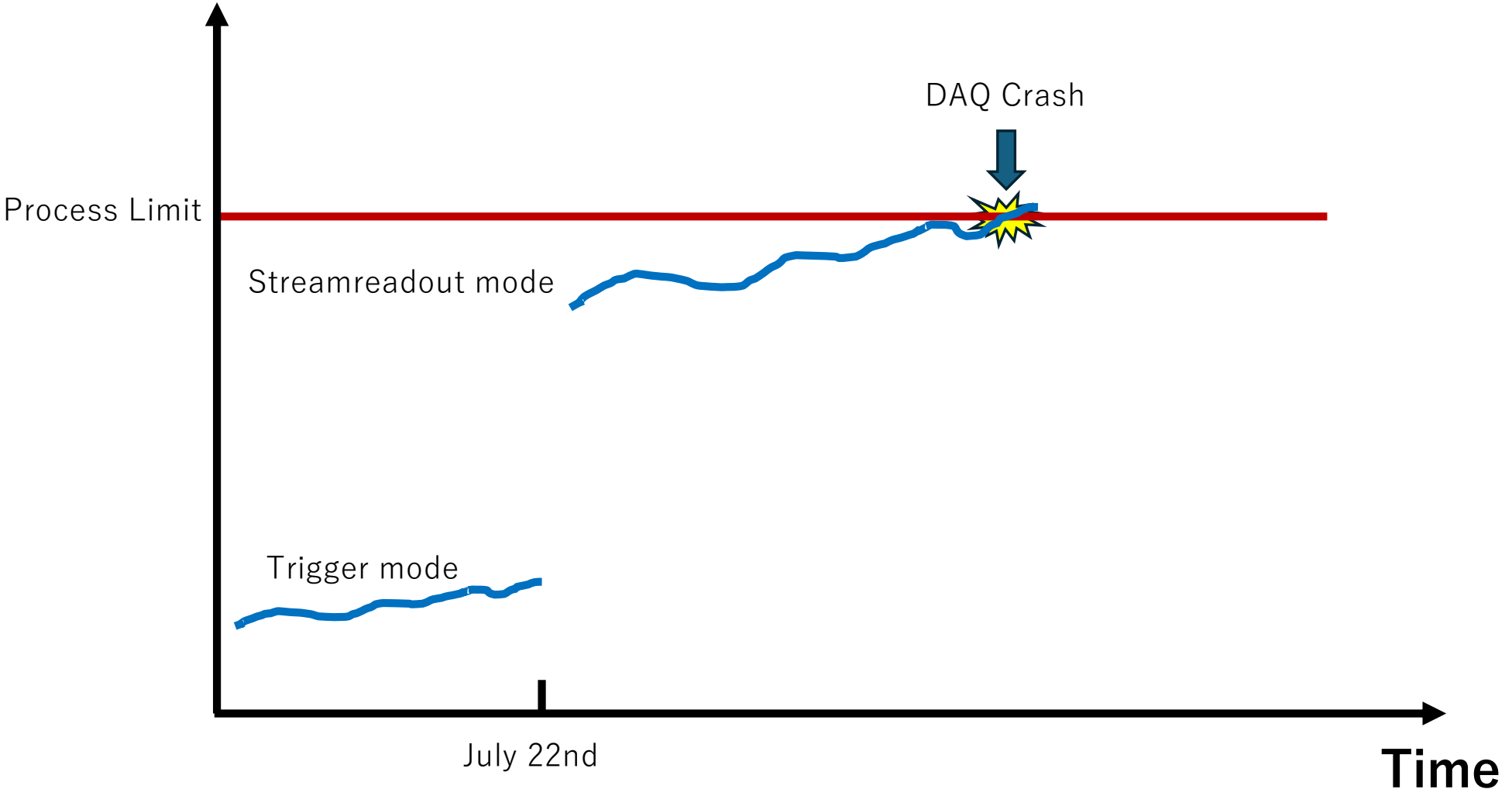
RCDAQ Disk Write Rate Timeline



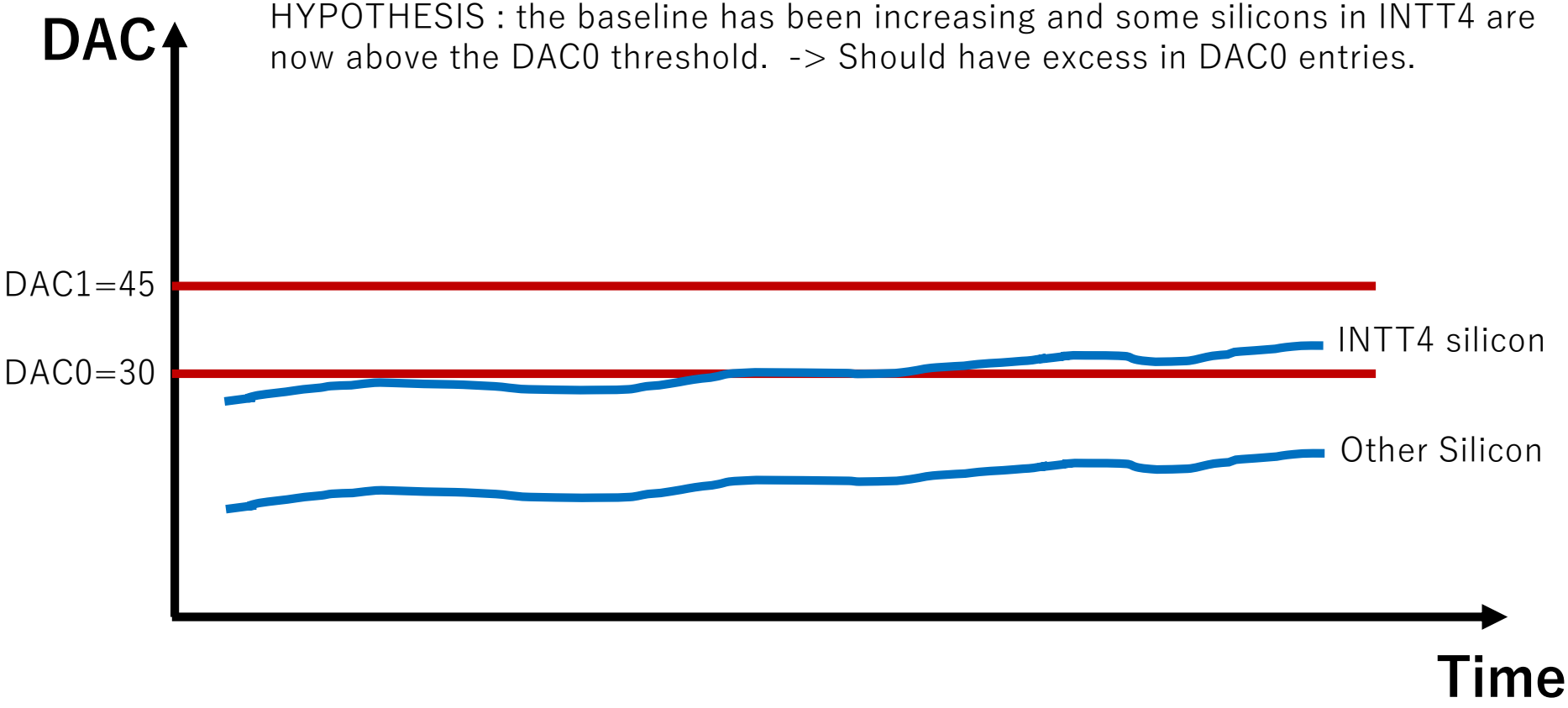


Process Hit Rate@Felix

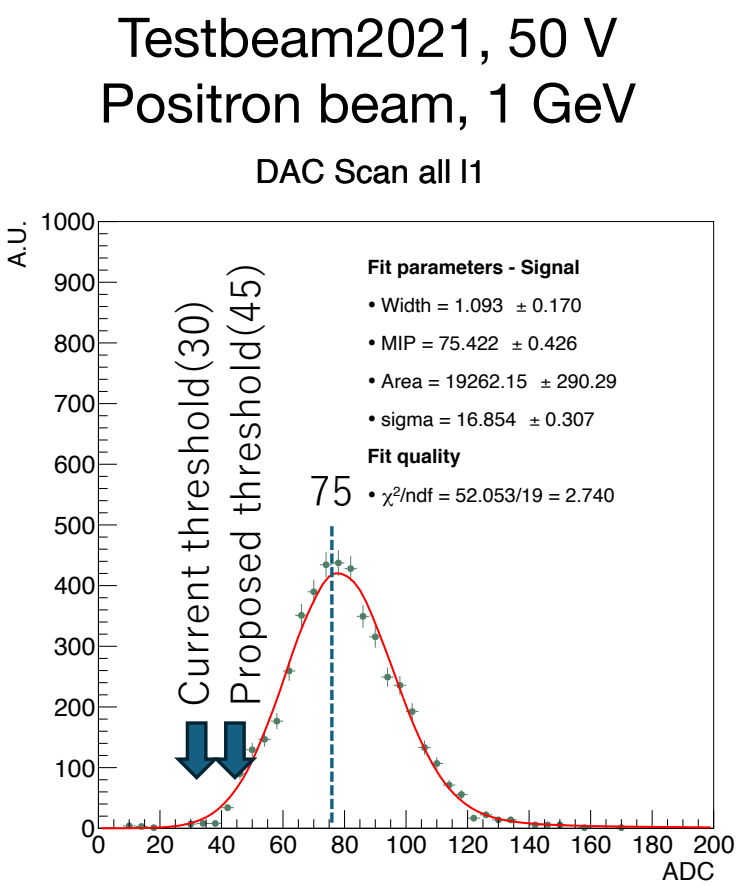


Why rate increased in INTT4?

HYPOTHESIS : the baseline has been increasing and some silicons in INTT4 are now above the DAC0 threshold. -> Should have excess in DAC0 entries.

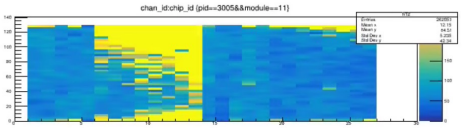


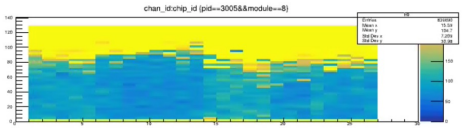
Efficiency vs. DAC0 Threshold



There are two options to restore 2 masked ladders

1. Masking all noisy strips -> massive!
2. Raise thresholds only for troublesome chips





DAC [ch]	Sigma	Detection Efficiency [%]
30	2.8	99.5
45	2.6	96.5

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