DAQ Trouble of Stream readout Issue

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RCDAQ Disk Write Rate Timeline

RCDAQ Disk Write	e Rate Timeline		
gl1daq ebdc01 ebdc03 ebdc05 ebdc09 ebdc11 ebdc13 ebdc15 ebdc15 ebdc19 ebdc21 ebdc21 ebdc21 ebdc21 intt0 intt2 intt4 intt6 mutt0			
mvtx2 mvtx4 seb00 seb02 seb04 seb06 seb14 seb16 seb18			
07/18 00:00	07/19 00:00	07/20 00:00	0
0 B/s		380 MB/s	
Run	07/19 00:00	07/20 00:00	07





Why rate increased in INTT4?



Efficiency vs. DAC0 Threshold

Testbeam2021, 50 V Positron beam, 1 GeV



There are two options to restore 2 masked ladders 1. Masking all noisy strips -> massive!

2. Raise thresholds only for troublesome chips



DAC [ch]	Sigma	Detection Efficiency [%]
30	2.8	99.5
45	2.6	96.5

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