



APD HV Test

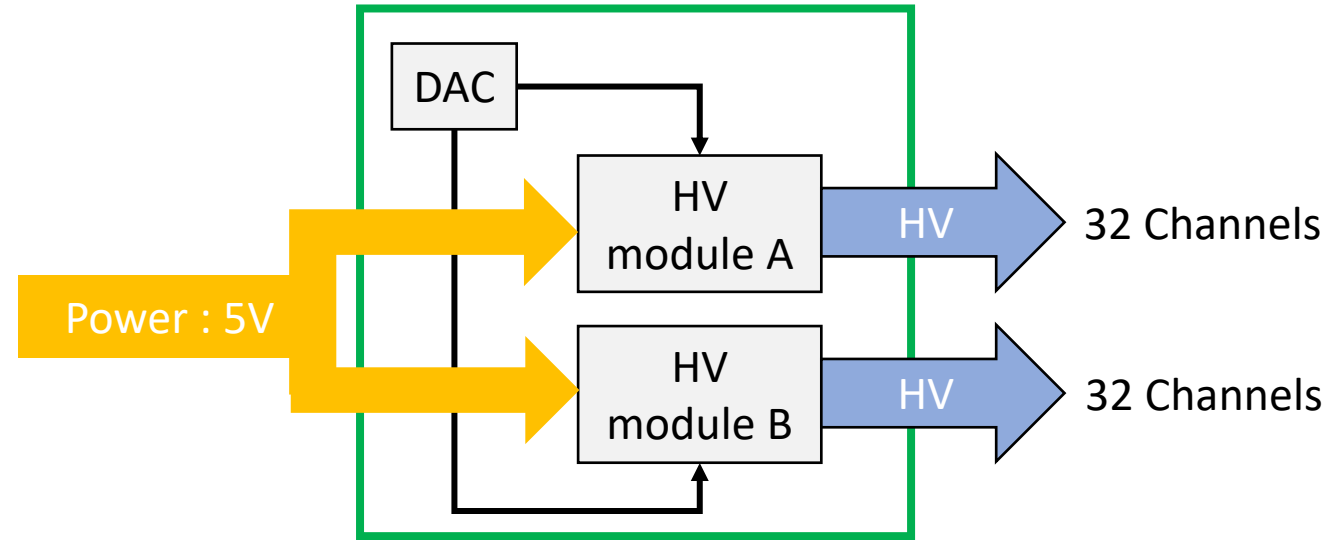
Cheng Kai-Yu

HV System

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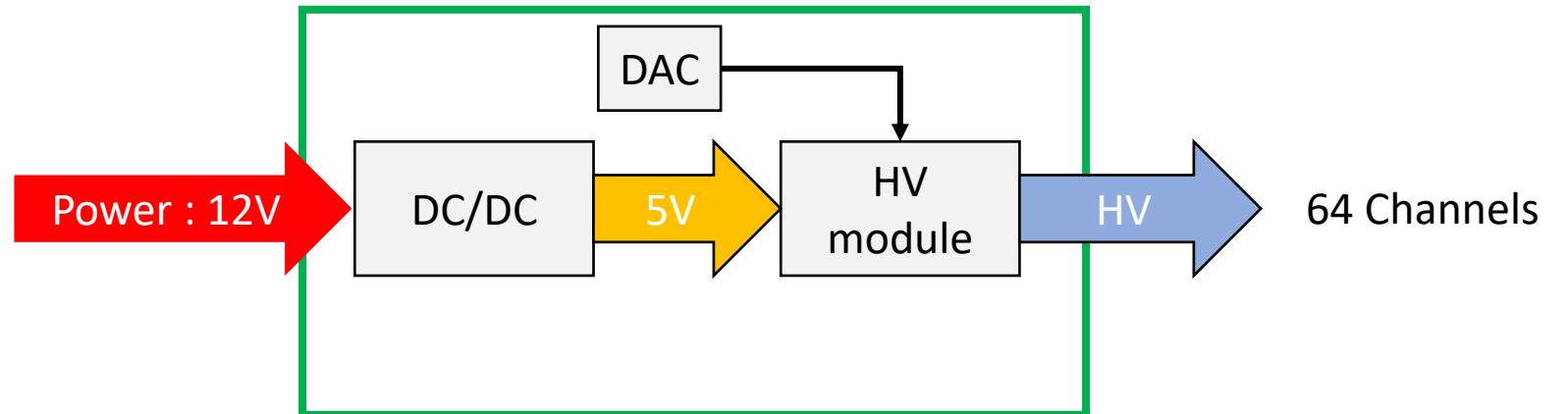
Previous

Power : 5V
Directly input to HV modules

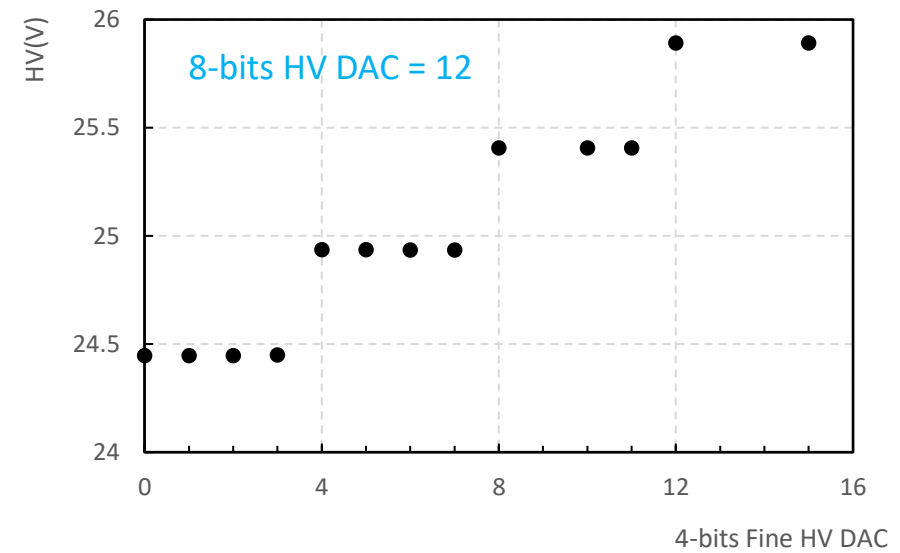
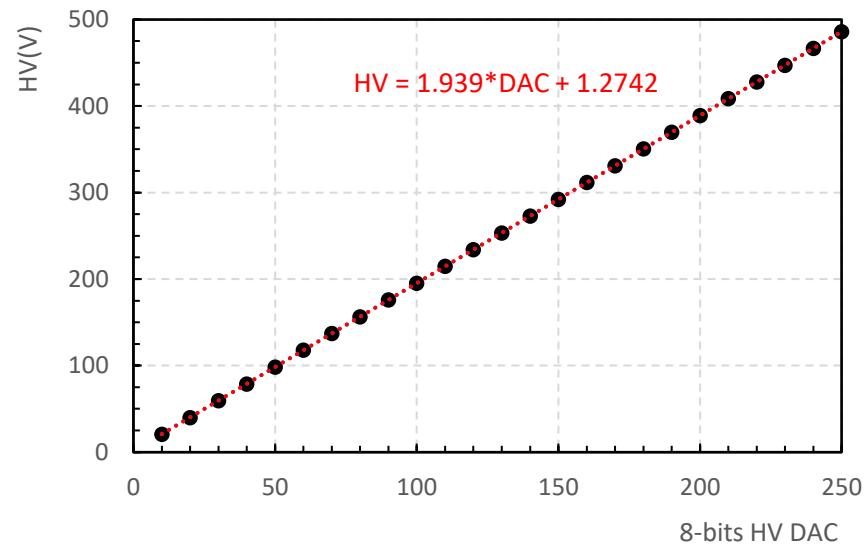


New

Power : 12V
Reducing the effect for HV output



- $HV = 1.939V \times DAC + 1.274V$
- $1DAC = 1.939V$
- The minimum adjustment voltage = $1.939V/4 \cong 0.485V$



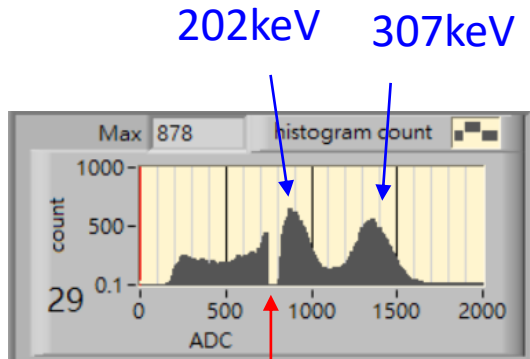
Test with GAGG+LYSO

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- Module: GAGG + SiPM
- Source : LYSO
- All channels work

ROC A

ROC B



LG/HG display gap